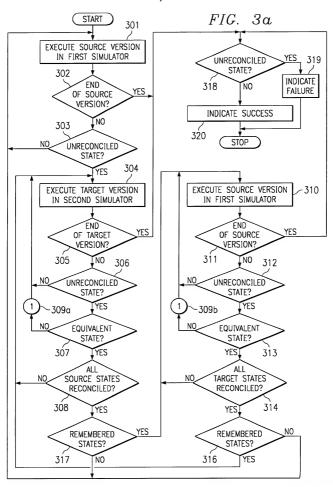
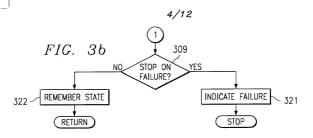


	403	405		404	406
SOURCE EXECUTION	SOURCE	SOURCE STATE	TARGET EXECUTION	TARGET	TARGET STATE
<i>→</i>	STM data1, AR3		₹	MOV data1, AR3	
401	LD #1, A		402	MOV #1, ACO	
	NOP	EMPTY		NOP	EMPTY
	NOP			NOP	
	STL A, *AR3+			MOV ACO<<#0, AR3+	
	NOP			NOP	

FIG.  $4\alpha$ 





	403	405 F	IG. 48	404	406
SOURCE EXECUTION	SOURCE	SOURCE STATE	TARGET EXECUTION	TARGET	TARGET STATE
	STM data1, AR3		<b>†</b>	MOV data1, AR3	
7*	LD #1, A		402	MOV #1, ACO	
401	NOP	AR3=0x800		NOP	EMPTY
	NOP			NOP	
	STL A, *AR3+			MOV ACO<<#0, AR3+	
	NOP			NOP	

	403	405	FIG. 4	C 404	406
SOURCE EXECUTION	SOURCE	SOURCE STATE	TARGET EXECUTION	TARGET	TARGET STATE
	STM data1, AR3			MOV data1, AR3	
7*	LD #1, A		7	MOV #1, ACO	XAR3=
401	NOP	AR3=0x800	402	NOP	0x430
	NOP			NOP	
	STL A, *AR3+			MOV ACO<<#0, AR3+	
	NOP			NOP	

	403	405		404	406
SOURCE EXECUTION	SOURCE	SOURCE STATE	TARGET EXECUTION	TARGET	TARGET STATE
	STM data1, AR3			MOV data1, AR3	
	LD #1, A	AR3=0x800	7	MOV #1, ACO	XAR3=
7	NOP	A=1	402	NOP	0x430
401	NOP			NOP	
	STL A, *AR3+			MOV ACO<<#0, AR3+	
	NOP			NOP	

FIG. 4d

	403	405		404	406
SOURCE EXECUTION	SOURCE	SOURCE STATE	TARGET EXECUTION	TARGET	TARGET STATE
	STM data1, AR3			MOV data1, AR3	
	LD #1, A	AR3=0x800		MOV #1, ACO	XAR3=
7►	NOP	A=1	7	NOP	0x430 AC0=1
401	NOP		402	NOP	
	STL A, *AR3+			MOV ACO<<#0, AR3+	
	NOP			NOP	

FIG. 4e

	403	405		404	406
SOURCE EXECUTION	SOURCE	SOURCE STATE	TARGET EXECUTION	TARGET	TARGET STATE
	STM data1, AR3			MOV data1, AR3	
	LD #1, A	AR3=0x800		MOV #1, ACO	XAR3=
	NOP	(*AR3)=1	₹	NOP	0x430
	NOP		402	NOP	
401	STL A, *AR3+			MOV ACO<<#0, AR3+	
7	NOP			NOP	

FIG. 4f

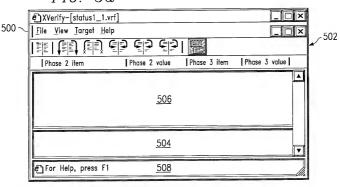
	403	405		404 /	406
SOURCE EXECUTION	SOURCE	SOURCE STATE	TARGET EXECUTION	TARGET	TARGET STATE
	STM data1, AR3			MOV data1, AR3	
	LD #1, A	AR3=0x800		MOV #1, ACO	XAR3=
	NOP	(*AR3)=1		NOP	0x430 (*XAR3)=1
	NOP			NOP	, , ,
401	STL A, *AR3+		402	MOV ACO<<#0, AR3+	
7	NOP		7	NOP	

FIG. 4g

# FIG. 4h

	403	405		404 /	406
SOURCE EXECUTION	SOURCE	SOURCE STATE	TARGET EXECUTION	TARGET	TARGET STATE
	STM data1, AR3			MOV data1, AR3	
	LD #1, A			MOV #1, ACO	
	NOP	EMPTY		NOP	EMPTY
	NOP			NOP	
401	STL A, *AR3+		402	MOV ACO<<#0, AR3+	
7	NOP		7	NOP	

FIG. 5a



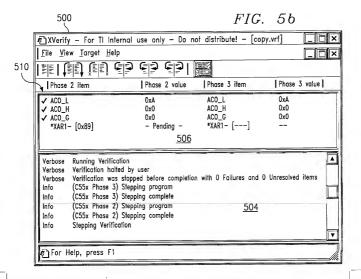
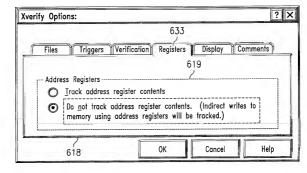


FIG. 6a

Xverify	y Options:					?	×
6	630	631	632	633	634	635	
	Files 1			Registers		Comments	7
11 3	File:						
	c54c	m1_p2.out		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,			
	Execute 0	n: <u>S</u> imulator	<u> </u>	nulator			
Ī	·Target Pro	ogram					
	c54c	m1_p3.out					
	Execute 0	n: Simulator	O E	<u>m</u> ulator			
	Rur	Code Con	nposer in the	Background running XVe	while runnin	:	
	60	0		ОК	Cancel	Help	The state of the s

FIG. 6d



## FIG. 6b

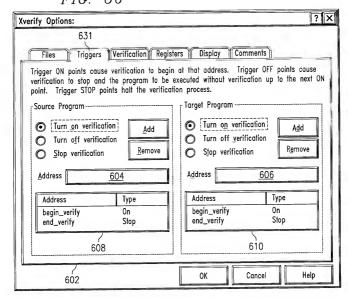
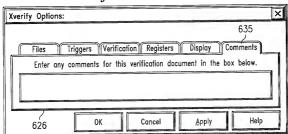


FIG. 6f



#### FIG. 6c 11/12

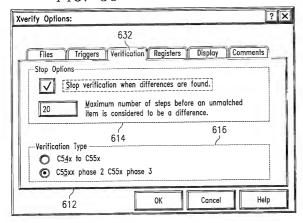


FIG. 6e

